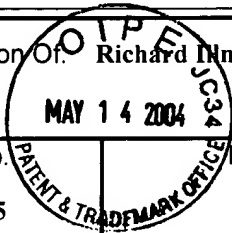


**SUBMISSION OF PROPOSED DRAWING AMENDMENT FOR APPROVAL BY  
EXAMINER (37 CFR 1.121(a) or 37 CFR 1.121(b)(3)(ii))**

Docket No.  
CAF-27202/03

In Re Application Of: Richard Inman



Serial No.  
09/808,325

Filing Date  
3/14/01

Examiner  
Marshall Eng

Art Unit  
2133

Invention: **METHOD FOR TESTING FOR THE PRESENCE OF FAULTS IN DIGITAL CIRCUITS**

Address to:  
Commissioner for Patents  
P.O. Box 1450  
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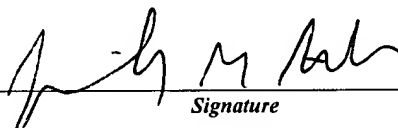
Technology Center 2100

Attached please find:

*(check applicable items)*

- ☐ a sketch in permanent ink
- ☒ a copy of the original drawing(s) with red ink

showing proposed changes to the drawing(s) in this application for which the approval of the examiner is requested.

  
\_\_\_\_\_  
Signature  
Judith M. Riley, Reg. No. 31,561  
Gifford, Krass, Groh, Sprinkle, Anderson  
& Citkowski, P.C.  
280 N. Old Woodward Ave., Ste. 400  
Birmingham, MI 48009  
US

Dated: 5/14/04

I certify that this document and fee is being deposited  
on 5/14/04 with the U.S. Postal Service as  
first class mail under 37 C.F.R. 1.8 and is addressed to the  
Commissioner for Patents, P.O. Box 1450, Alexandria, VA  
22313-1450.

  
\_\_\_\_\_  
Signature of Person Mailing Correspondence

Sharon Kaiser

Typed or Printed Name of Person Mailing Correspondence